IN THE CLAIMS:

Claims 1 - 21 are pending in the application.

1. (currently amended) A liquid crystal display device comprising: a pair of substrates with having a liquid crystal layer disposed therebetween; at least a first conductive layer formed on one of said pair of substrates; at least a first insulating layer formed on the first conductive layer; a plurality of drain signal lines formed on the first insulating layer with in overlapping relation to the first conductive layer;

at least a second insulating layer formed on the a drain signal line; at least a second conductive layer formed on the second insulating layer and

elongated substantially along the drain signal line with in overlapping relation to the drain signal line;

wherein the second conductive layer is stand off offset from the overlapping region of the first conductive layer and the drain signal line.

- 2. (currently amended) A liquid crystal display device according to claim 1, wherein the second conductive layer maintain maintains an electrical connection around the stand off offset region.
- 3. (currently amended) A liquid crystal display device according to claim 2, further comprising a plurality of gate signal lines formed on the one of said pair of substrates and crossing to the drain signal lines,

wherein the second conductive layer include includes a portion having an

overlapping relation with the gate signal line.

4. (currently amended) A liquid crystal display device according to claim 3, wherein the second insulating layer include-includes a lower insulating layer and an upper insulating layer formed on the lower insulating layer and made by of an organic material,

wherein the upper insulating layer is stand off offset from the overlapping region of the first conductive layer and the drain signal line.

- 5. (currently amended) A liquid crystal display device according to claim 4, wherein an area of standing off the offset of the second conductive layer is bigger than an area of standing off the offset of the upper insulating layer.
- 6. (currently amended) A liquid crystal display device according to claim 3, wherein the second insulating layer include-includes a lower insulating layer made by of an inorganic material and an upper insulating layer formed on the lower insulating layer and made by of an organic material,

wherein the upper insulating layer is stand off offset from the overlapping region of the first conductive layer and the drain signal line and the lower insulating layer is not stand off offset from the overlapping region.

7. (currently amended) A liquid crystal display device according to claim 1, further comprising a plurality of gate signal lines formed on the one of said pair of substrates and crossing to the drain signal lines,

wherein the first conductive layer is the a gate signal line.

- 8. (currently amended) A liquid crystal display device according to claim 7, wherein the gate signal line is separated to plural <u>lines</u> at the <u>region of</u> overlapping region to of the drain signal line.
- 9. (currently amended) A liquid crystal display device according to claim 1, further comprising a plurality of counter signal lines formed on the one of said pair of substrates and crossing to the drain signal lines,

wherein the first conductive layer is the a counter signal line.

- 10. (currently amended) A liquid crystal display device according to claim 9, wherein the counter signal line is separated to plural <u>lines</u> at the <u>region of</u> overlapping <u>region to of</u> the drain signal line.
- 11. (currently amended) A liquid crystal display device comprising:

a pair of substrates with having a liquid crystal layer disposed therebetween;

a plurality of gate signal lines and at least a first conductive layer formed on one of said pair of substrates;

at least a first insulating layer formed on the a gate signal line;

a plurality of drain signal lines formed on the first insulating layer and crossing to the gate signal lines to form plural pixels line;

at least a second insulating layer formed on the a drain signal line; wherein the first conductive layer is elongated substantially along the drain

signal line and having a portion overlapping portion to the drain signal line; and at least a second conductive layer formed on the second insulating layer and elongated substantially along the drain signal line with in overlapping relation to the drain signal line and the first conductive layer;

a the width of the second conductive layer at the overlapping region of the drain signal line and the first conductive layer is smaller than not a non-overlapping region of the drain signal line and the first conductive layer.

- 12. (currently amended) A liquid crystal display device according to claim 11, wherein the overlapping region of the drain signal line and the first conductive layer is plural in each of the pixels, and athe width of the second conductive layer make is smaller in each overlapping region.
- 13. (currently amended) A liquid crystal display device according to claim 11, wherein the second conductive layer stand off is offset from the first conductive layer at the overlapping region of the first conductive layer and the drain signal line, and an overlap to the another first conductive layer is arranged on an opposite side of the drain signal line relative to the first conductive layer having an overlapping relation with the drain signal line.
- 14. (currently amended) A liquid crystal display device according to claim 11, wherein the second insulating layer include includes a lower insulating layer made byof an inorganic material and an upper insulating layer formed on the lower insulating layer and made byof an organic material,

wherein the upper insulating layer is stand off offset from the overlapping region of the first conductive layer and the drain signal line.

15. (currently amended) A liquid crystal display device comprising:

a pair of substrates with having a liquid crystal layer disposed therebetween; at least a first conductive layer formed on one of said pair of substrates; at least a first insulating layer formed on the first conductive layer;

a plurality of drain signal lines formed on the first insulating layer with in overlapping relation to the first conductive layer;

at least a second insulating layer formed on the a drain signal line;

at least a second conductive layer formed on the second insulating layer and elongated substantially along the drain signal line with in overlapping relation to the drain signal line;

wherein the second conductive layer have has a hole at the overlapping region of the first conductive layer and the drain signal line.

16. (currently amended) A liquid crystal display device according to claim 15, further comprising a plurality of gate signal lines formed on the one of said pair of substrates and crossing to the drain signal lines,

wherein the second conductive layer include includes a portion having an overlapping relation with the a gate signal line.

17. (currently amended) A liquid crystal display device according to claim 15, wherein the second insulating layer include includes a lower insulating layer and an

upper insulating layer formed on the lower insulating layer and made byof an organic material,

wherein the upper insulating layer have has a hole as the overlapping region of the first conductive layer and the drain signal line.

- 18. (original) A liquid crystal display device according to claim 17, wherein the hole of the second conductive layer is bigger than the hole of the upper insulating layer.
- 19. (currently amended) A liquid crystal display device according to claim 15, wherein the first conductive layer is separated to plural <u>lines</u> at the <u>region of</u> overlapping <u>region to of</u> the drain signal line.
- 20. (currently amended) A liquid crystal display device according to claim 19, further comprising a plurality of gate signal lines formed on the one of said pair of substrates and crossing to the drain signal lines,

wherein the first conductive layer is the a gate signal line.

21. (currently amended) A liquid crystal display device according to claim 19, further comprising a plurality of counter signal lines formed on the one of said pair of substrates and crossing to the drain signal lines,

wherein the first conductive layer is the a counter signal line.